

HYBRID OPEN AND FOLDED DIGIT LINE ARCHITECTURE

ABSTRACT OF THE DISCLOSURE

A digit line architecture for an array of memory cells exhibiting characteristics of both folded digit line architectures and an open digit line architectures. The digit line architecture includes first and second digit lines having first and second digit line segments. The memory cells of a column are coupled to the first digit line segments. The second digit line segment of the first digit line is located in the memory sub-array with which the column is associated and the second digit line segment of the second digit line extending into the other memory sub-array with which the column is not associated.

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4836-2082-4576\1